

EXPEDITED PROCEDURE
RESPONSE UNDER 37 CFR § 1.116--EXAMINING GROUP 2811

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application: Divakaruni et al.

Today's Date: October 22, 2002

Serial No.: 09/718,850

Group Art Unit: 2811

Filed: 11/22/2000

Examiner: Gebremariam, Samuel A.

Title: LOGIC SOI STRUCTURE, PROCESS AND APPLICATION FOR VERTICAL
BIPOLAR TRANSISTOR

AMENDMENT AFTER FINAL UNDER 37 C.F.R. 1.116

FAX COPY RECEIVED

To the Assistant Commissioner of Patents
Washington, D.C. 20231

OCT 24 2002 11/20/02

TECHNOLOGY CENTER 2800

Dear Sir:

In response to the Office Action of August 28, 2002, in which all of pending claims 1-5 and 7-20 stand rejected, Applicants respectfully request that the subject U.S. patent application be amended as follows. The Patent Office is hereby authorized to charge Applicants' Deposit Account 09-0456 for any fees as may be due for the entry and consideration of this amendment.

10/30/2002 SSURLES 00000005 090456 09718850

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84.00 CH		CERTIFICATE OF MAILING	
I hereby certify that, on the date shown below, this correspondence is being:			
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<input type="checkbox"/> deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, DC 20231.		<input checked="" type="checkbox"/> transmitted by facsimile to the Patent and Trademark Office. 703-872-9319	
Date: _____		PAT BLAIR Name Signature	

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